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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | Complete if Known | | | |
| | | Application Number | 10/698,851 | | |
| | | Filing Date | October 31, 2003 | | |
| | | First Named Inventor | Surya Varanasi | | |
| | | Art Unit | 2600 2616 | | |
| | | Examiner Name | Not yet assigned Farah Faroul | | |
| Sheet | 1 | of | 1 | Attorney Docket Number | 003.P002 |

| NON PATENT LITERATURE DOCUMENTS | | | |
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| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| /FF/ | 1 | LINK TRUNKING AND MEASURING LINK LATENCY IN FIBRE CHANNEL FABRIC, Inventors: David C. Banks, et al., Application No: 10/135,615, Filed: 4/30/02, 39 Pgs. | |
| /FF/ | 2 | LOAD BALANCING IN A NETWORK COMPRISING COMMUNICATION PATHS HAVING DIFFERENT BANDWIDTHS, Application No: 10/208,969, Filed: 7/31/02, 24 Pgs. | |
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| Examiner Signature | /Farah Faroul/ | Date Considered | 10/05/2007 |
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